FORM PTO-1449

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(Use several sheets if necessary)

Docket Number (Optional)	Application Number
81674-271623	09/664,910
Applicant JOHN HALBERT, et al.	
Filing Date September 18, 2000	**Group Art Unit** 2182

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
DG	RE 34455	11-16-93	Hayes et al.	—	—	
DG	5 613 153	3-18-97	Arimilli et al.	—	—	
DG	5 961 653	10-5-99	Kalter et al.	—	—	

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation YES	Translation NO

OTHER DOCUMENTS ***(Including Author, Title, Date, Pertinent Pages, Etc.)***

DG	United States Patent Application Publication No. US 2001/0014925A1, Aug. 16, 2001, Kumata
DG	A 72K CMOS Channelless Gate Array with Embedded 1Mbit Dynamic RAM, Kazuhiro Sawada, et al., pp. 20.3.1 - 20.3.4.

EXAMINER	DATE CONSIDERED
[signature]	6/25/2003

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DG	5 9 8 2 6 8 1	11-9-99	Schwarz	—	—	

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